



IN THE UNITED STATES PATENT OFFICE

Applicants: Tao Pi et al.
Assignee: Xilinx, Inc.
Title: FPGA Lookup Table with Transmission Gate
Structure for Reliable Low-Voltage Operation
Serial No.: 10/693,218 File Date: 10/24/2003
Examiner: Don P. Le Art Unit: 2819
Docket No.: X-1128-1D US Conf. No.: 2270

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed April 20, 2004,
Applicant submits the following remarks. A terminal
disclaimer is also filed herewith.

Claims are reflected in the listing of claims which begins
on page 2 of this paper. No amendments are made to the
claims in the present Response, but a listing of the claims
is included to facilitate prosecution.

Remarks begin on page 6 of this paper.